

TDA7388

4 x 45 W quad bridge car radio amplifier

Datasheet - production data Low external component count: Internally fixed gain (26 dB) - No external compensation No bootstrap capacitors **Protections:** Output short circuit to gnd, to V_S, across the • load Very inductive loads Overrating chip temperature with soft thermal Flexiwatt25 limiter Load dump voltage Fortuitous open GND **Reversed battery** • ESD **Features** Description High output power capability: The TDA7388 is an AB class audio power - 4 x 45 W / 4 Ω max. amplifier, packaged in Flexiwatt 25 and designed – 4 x 26 W / 4 Ω @ 14.4 V, 1 kHz, 10 % for high end car radio applications. Low distortion Based on a fully complementary PNP/NPN Low output noise configuration, the TDA7388 allows a rail to rail output voltage swing with no need of bootstrap Standby function capacitors. The extremely reduced boundary Mute function components count allows very compact sets. Automute at min. supply voltage detection Table 1. Device summary

Order code	Package	Packing
TDA7388	Flexiwatt25	Tube

Se	otem	ber	2013
00	P.0		2010

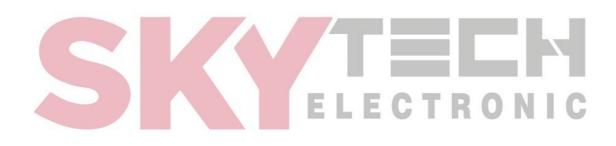
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This is information on a product in full production.

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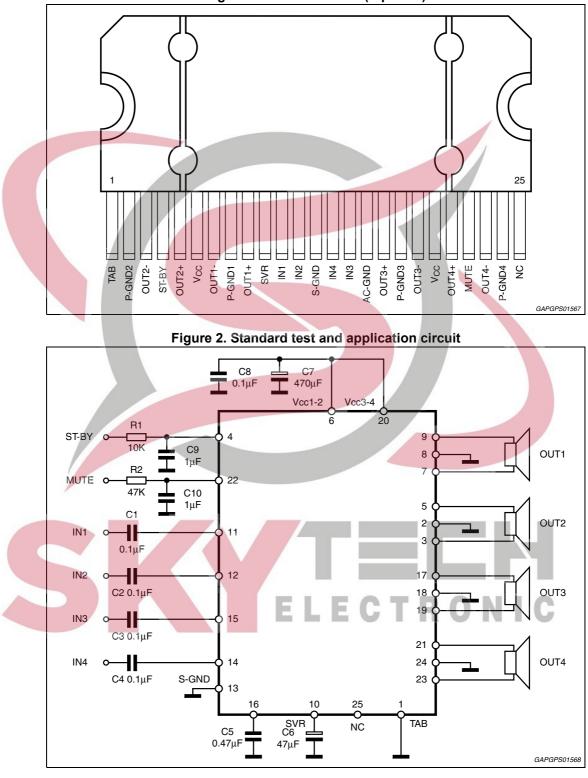
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Pin connection and test/application diagrams 1



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Figure 1. Pin connection (top view)



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2 Electrical specifications

2.1 Absolute maximum ratings

Symbol	Parameter	Value	Unit	
V _S Operating supply voltage		18	V	
V _{S (DC)}	DC supply voltage	28	V	
V _{S (pk)} Peak supply voltage (t = 50 ms)		50	V	
Io	Output peak current: Repetitive (duty cycle 10 % at f = 10 Hz) Non repetitive (t = 100 µs)	4.5 5.5	A	
P _{tot}	Power dissipation, (T _{case} = 70 °C)	80	W	
Тj	Junction temperature	150	°C	
T _{stg}	Storage temperature	– 55 to 150	°C	

Table 2. Absolute maximum ratings

2.2 Thermal data

	Table 3. Therma	Table 3. Thermal data		
Symbol	Parameter		Value	Unit
R _{th j-case}	Thermal resistance junction-to-case	max.	1	°C/W





2.3 Electrical characteristics

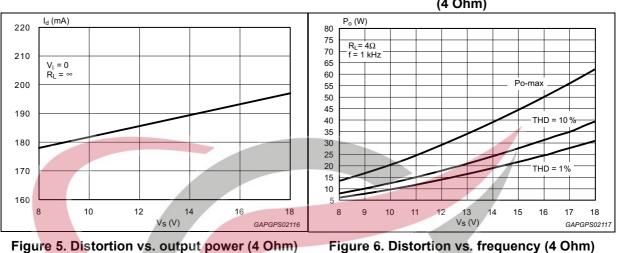
 V_S = 14.4 V; f = 1 kHz; R_g = 600 Ω ; R_L = 4 Ω ; T_{amb} = 25 °C; Refer to the test and application diagram (*Figure 2*), unless otherwise specified.

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
I _{q1}	Quiescent current	$R_L = \infty$	120	190	350	mA
V _{OS}	Output offset voltage	Play mode	-	-	±100	mV
dV _{OS}	During mute ON/OFF output offset voltage	ITU R-ARM weighted see <i>Figure 12</i>	-80	-	+80	mV
Gv	Voltage gain	-	25	26	27	dB
Po	Output power	THD = 10 %; V _S = 14.4 V	22	26	-	W
P _{o max}	Max.output power ⁽¹⁾	V _S = 14.4 V	37	41	-	w
F o max		V _S = 15.2 V	-	45	-	vv
THD	Distortion	$P_0 = 4 W$	-	0.04	0.15	%
	Output noise	"A" Weighted	-	50	70	μV
e _{No}	Output hoise	Bw = 20 Hz to 20 kHz	-	70	100	μV
SVR	Supply voltage rejection	f = 100 Hz; V _r = 1 Vrms	50	65	-	dB
f _{ch}	High cut-off frequency	P _o = 0.5 W	100	200	-	kHz
R _i	Input Impedance	-	70	100	-	kΩ
6	Cross tells	f = 1 kHz; P _o = 4 W	60	70	-	dB
C _T	Cross talk	f = 10 kHz; P _o = 4 W		60	-	dB
I _{SB}	Standby current consumption	V _{St-by} = 0V		-	20	μA
V _{SB out}	Standby OUT threshold voltage	(Amp: ON)	3.5	-	-	V
V _{SB IN}	Standby IN threshold voltage	(Amp: OFF)	-	-	1.5	V
A _M	Mute attenuation	P _{Oref} = 4 W	80	90	-	dB
V _{M out}	Mute OUT threshold voltage	(Amp: play)	3.5	-	7-	V
V _{M in}	Mute IN threshold voltage	(Amp: mute)	-	-	1.5	V
V _{AM in}	V _S automute threshold	(Amp: mute); Att. \ge 80 dB; P _{Oref} = 4 W (Amp: play); Att. < 0.1 dB; P _Q = 0.5 W	r R	O N 7.6	6.5 8.5	Cv
lai-oo	Muting pin current	V _{MUTE} = 1.2 V (Source current)	5	11	20	μA
I _{pin22}			5		20	۳A

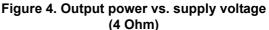
1. Saturated square wave output.

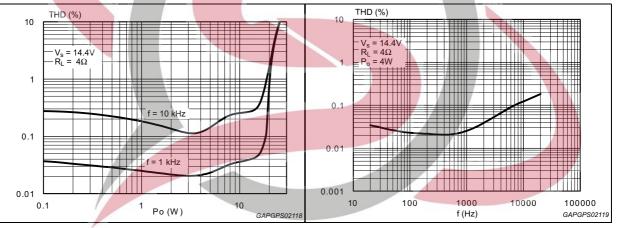


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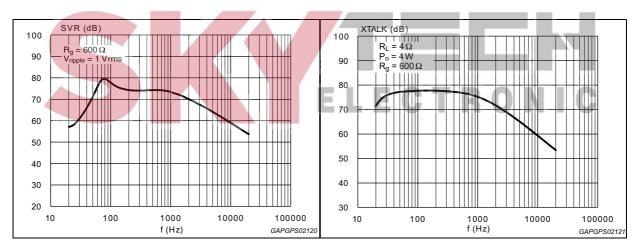












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η

Pdiss

η (%)

90

80

70

60

50

40

30

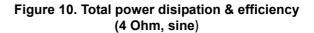
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GAPGPS02123



Figure 9. Output noise vs. source resistance



Pdiss(W)

 $V_{S} = 14.4 V$ $R_{L} = 4 \times 4 \Omega$ f = 1 kHz

90

80

70

60

50

40

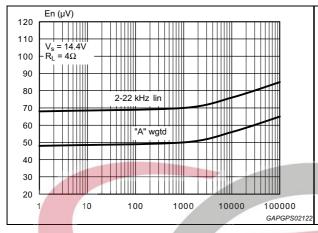
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20

10

0

0 2 4 6 8 10



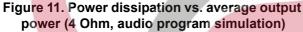
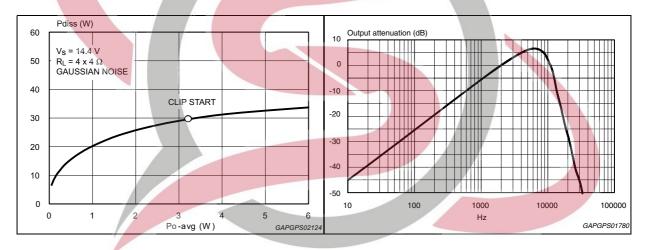


Figure 12. ITU R-ARM frequency response, weighting filter for transient pop

12 14 Po(W)

16 18 20 22 24 26







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3 Application hints

Ref. to the circuit of Figure 2.

3.1 SVR

Besides its contribution to the ripple rejection, the SVR capacitor governs the turn ON/OFF time sequence and, consequently, plays an essential role in the pop optimization during ON/OFF transients.

To conveniently serve both needs, its minimum recommended value is 10 µF.

3.2 Input stage

The TDA7388's inputs are ground-compatible and can stand very high input signals (±8 Vpk) without any performances degradation.

If the standard value for the input capacitors (0.1 μ F) is adopted, the low frequency cut-off amounts to 16 Hz.

3.3 Standby and muting

If standby and muting are not used, a straight connection to V_S of their respective pins would be admissible.

Conventional/low-power transistors can be employed to drive muting and standby pins in absence of true CMOS ports or microprocessors. R-C cells have always to be used in order to smooth down the transitions for preventing any audible transient noises.

Since a DC current of about 10 μ A normally flows out of pin 22, the maximum allowable muting-series resistance (R₂) is 70 k Ω , which is sufficiently high to permit a muting capacitor reasonably small (about 1 μ F).

If R_2 is higher than recommended, the involved risk is that the voltage at pin 22 may rises to above the 1.5 V threshold voltage and the device consequently fails to turn OFF when the mute line is brought down.

About the stand-by, the time constant to be assigned in order to obtain a virtually pop-free transition has to be slower than 2.5 V/ms.



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4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: <u>www.st.com</u>.

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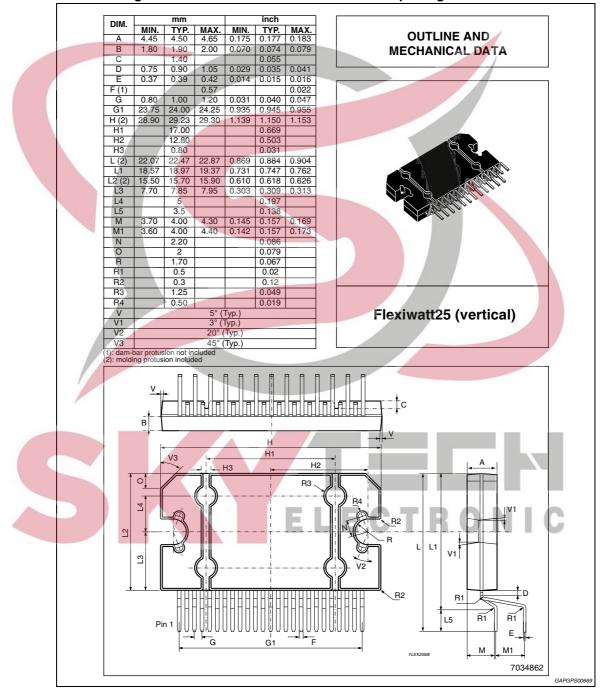


Figure 13. Flexiwatt25 mechanical data and package dimensions



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5 Revision history

	Date	Revision	Changes
	06-Dec-2007	1	Initial release.
	12-Jul-2010	2	Document status promoted from preliminary data to datasheet.
	26-Apr-2012	3	Modified <i>Features on page 1.</i> Updated <i>Table 4: Electrical characteristics on page 7.</i>
	20-Jun-2012	4	Updated Section 3.3: Standby and muting.
	11-Mar-2013	5	Added Section 2.4: Electrical characteristic curves.
1	17-Sep-2013	6	Updated Disclaimer.



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